

Figure ~~1A~~ 1A
Block diagram of partial product generation for prior art Booth radix-4 multiplier

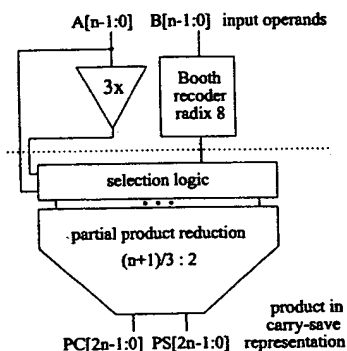


Figure ~~1B~~ 1B
Block diagram of partial product generation for prior art Booth radix-8 multiplier

PRIOR
ART

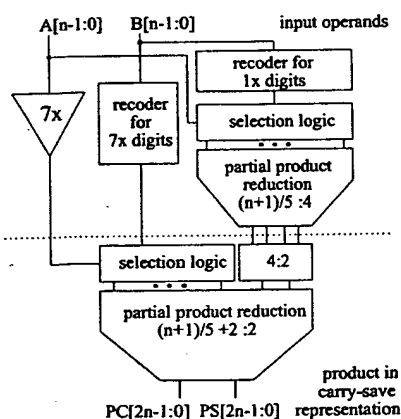


Figure ~~2A~~ 2A
Simplified block diagram of partial product generation for radix-32 recoding with precomputation of 7x multiplier
multiplier
multiplier

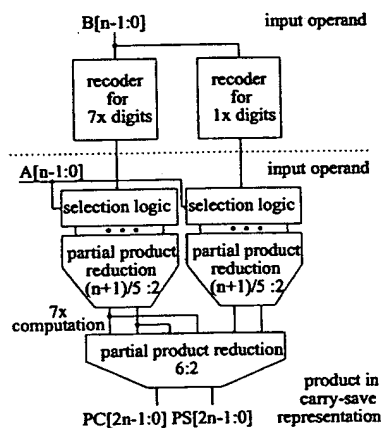


Figure ~~2B~~ 2B
Simplified block diagram of partial product generation for preferred embodiment radix-32 encoding with precomputation of 7x multiplier
multiplier

postcomputation by